



Image

PATENT

1742

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

TALIEH et al.

Serial No.: 09/941,360

Filed: August 28, 2001

Title: Method And Apparatus For Plating and  
Polishing A Semiconductor Substrate

Group Art Unit: 1742  
Examiner: D. Valentine  
Docket: NT-004D-US

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the US Postal Service as First Class Mail in a postage-paid envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 30, 2004.

Signed:

Amy Alwine

**RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This is a response to a Notice of Non-Compliant Amendment dated January 22, 2004 indicating that a complete listing of all of the claims is not present, and all claims must be submitted with their current status indicators. Applicants resubmit the listing of claims on page 2 of this paper and have changed the nomenclature of the status indicators to "currently amended", as required.

As indicated in the Office Action mailed July 2, 2003, only claims 20-27 and 30-33 are pending in the application. Accordingly, claims 20-27 and 30-33 were submitted with their current status indicators. However, attached is a copy of the entire listing of the claims consistent with the listing of claims, which were presented in Applicants' amendment mailed October 22, 2003.

Please change the correspondence address to:

NuTool, Inc.  
1655 McCandless Drive  
Milpitas, CA 95035

Respectfully submitted,

By: Daniel Hopen  
Daniel Hopen  
Reg. No. 35,547

Legal Department  
NuTool, Inc.  
1655 McCandless Drive  
Milpitas, CA 95035  
(408) 586-9500x268